



PTO/SB/21 (08-00)

## **TRANSMITTAL FORM**

**Application Number** 10/815,654 Filing Date April 2, 2004 **First Named Inventor** Shunpei YAMAZAKI et al. **Group Art Unit Examiner Name** M. Guerrero

(to be used for all correspondence after initial filing)

Total Number of Pages in This :	Submission	Attorney Docket Numb	er 0/56-72/9		
ENCLOSURES (check all that apply)					
Fee Transmittal Form Fee Attached Amendment After Final Affidavits/declaration(s Extension of Time Reques Express Abandonment Re Information Disclosure Sta Certified Copy of Priority Document(s) Response to Missing Parts Incomplete Application Response to Missing F under 37 CFR 1.52 or	t quest tement	Assignment Papers (for an Application) Drawing(s) Declaration and Power of Attorney Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Request for Refund CD, Number of CD(s) Remarks The Commissioner is fees required or credit any	After Allowance Communication to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosures 1. Response to Restriction Requirement 2. 3. 4. 5. 6.		
		2280 for the above identified docket number.			
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT					
Firm or Individual name Signature	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165				
Date	December 28, 2005				
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Signature		Rose Dichard	Date December 28, 2005		

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	Group Art Unit: 2822			
Shunpei YAMAZAKI et al.	)	Examiner: M. Guerrero			
Serial No. 10/815,654 Filed: April 2, 2004 For: SEMICONDUCTOR DEVICE,		CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450.			
			METHOD OF FABRICATING SAME,	)	Alexandria, VA 22313-1450, on December 28 2005
			AND ELECTROOPTICAL DEVICE	)	Rose Dichard

## RESPONSE TO RESTRICTION REQUIREMENT

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement in the Office Action of November 28, 2005, Applicant hereby elects with traverse Species 1D, a semiconductor device comprising a pair of flexible substrates and a TFT covered by a layer comprising resin. Independent claims 44, 48, 52 and 56 and their dependent claims read on the elected species and claims 27-42 and 72-79 are generic in that they are not limited to a liquid crystal device or a pair of substrates.

Examination on the merits is requested.

Respectfully submitted,

Eric J. Robinson

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